

<b>INFORMATION DISCLOSURE STATEMENT</b> <b>PTO-1449 (PAGE 1 OF 1)</b>		SERIAL NUMBER 10/045,017		DOCKET NO. P56664			
		APPLICANT YOSHITAKA TERAO et al.					
		FILING DATE 15 January 2002		GROUP 2875			
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
Am	RE37,444	11/01	Kanazawa				
	5,541,618	07/96	Shinoda				
	5,661,500	08/97	Shinoda et al.				
	5,663,741	09/97	Kanazawa				
	5,674,553	10/97	Sinoda et al.				
	5,724,054	03/98	Shinoda				
	5,786,794	07/98	Kishi et al.				
	5,952,782	09/99	Nanto				
	6,630,916	10/03	Shinoda				
	6,707,436	03/04	Setoguchi et al.				
<b>FOREIGN PATENT DOCUMENTS</b>						<b>TRANSLATION</b>	
	DOCUMENT NUMBER	DATE	COUNTRY	CLAS	SUBCLASS	YES	NO
Am	JP 2001-043804 /	02/01	JAPAN			Abstract	
	JP 2917279 /	04/99	JAPAN			Abstract	
	JP 02-148645 /	06/90					
	JP 2845183 /	10/98					
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
Am	<p>"Final Draft International Standard", Project No. 47C/61988-1/Ed.1; Plasma Display Panels - Part 1: Terminology and letter symbols, published by International Electrotechnical Commission, IEC. in 2003, and Appendix A - Description of Technology, Annex B - Relationship Between Voltage Terms And Discharge Characteristics; Annex C - Gaps and Annex D - Manufacturing</p>						
EXAMINER: <i>Pats MacLean</i>	DATE CONSIDERED: 10-14-04						
<small>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							